

PentiumPro / Pentium II / Pentium III / Pentium 4 / Celeron Microcode Patch Update Diskette

Version	1.01	flashbio.exe	V5.54
Release	1.17	bioflash.exe	V2.13
Date	14.11.2001		

P6 patch file name convention: p6_xyyy.zzz

xyyy: CPU_ID (x:overdrive bit)

zzz: Platform Id (000-Platform ID 0 (Slot1), 002- Platform ID 2 (Slot 2), 003- Platform ID 3(Mobile Module), 004- Platform ID 4 (FCPGA)), Reference: CPU MSR

Note: Only FSC boards designed by FSC Augsburg are supported.

File	Cpu type	Stepping	Code name	Patch id
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Platform ID: 0 (Slot 1):

P6_0611.000	Pentium Pro CPU	B0		B0026h
P6_0612.000		C0		C6h
P6_0616.000		A0		C6h
P6_0617.000		A1		C6h
P6_0619.000		B1		D2h

P6_1632.000	Pentium Pro	Overdrive		02h
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P6_0630.000	Pentium II	A-1	Klamath	13h
P6_0632.000		B-0		20h
P6_0633.000		C-0		34h
P6_0634.000		C-1		35h
P6_0650.000		dA-0	Deschutes	40h
P6_0651.000		dA-1		40h
P6_0652.000		dB-0		2Ah
P6_0653.000		dB-1		10h
P6_0660.000	Celeron	mA-0	Mendocino	0Ah

P6_0670.000	Pentium III	kA-0	Katmai	06h
P6_0671.000		kA-1		03h
P6_0672.000		kB-0		10h
P6_0673.000		kC-0		0Eh

P6_0680.000	Pentium III	cA-0	Coppermine	14h
P6_0681.000		cA-2		0Dh
P6_0683.000		cB-0		13h
P6_0686.000		cC-0		07h

P6_0690.000	Celeron	A-0	Timna	04h
P6_0691.000		A-1		02h

P6_0F00.000	P8	A-0	Willamette	01h
P6_0F01.000		A-1		04h
P6_0F02.000	Pentium 4	A-2	Willamette	0Bh
P6_0F03.000		A-3		01h
P6_0F04.000		wB-0		0Ah
P6_0F05.000		wB-1		07h
P6_0F07.000		wB-2		07h
P6_0F0A.000		wC-1		05h
P6_0F12.000		wD-0		01h

Platform ID: 1

P6_OF04.001	Pentium 4	B-0 special	Foster	0Bh
P6_OF05.001		B-1 special		0Ch
P6_OF06.001		B-0		04h
P6_OF09.001		B-1		08h
P6_OF0A.001		C-1		06h

P6_OF11.001	Pentium 4 Xeon	C-0	Foster	05h
P6_OF12.001	Pentium 4 Xeon	D-0	Foster	02h
P6_OF21.001	Pentium 4 Xeon	A-1	Prestonia	02h
P6_OF23.001	Pentium 4 Xeon	A-2	Prestonia	09h
P6_OF24.001	Pentium 4 Xeon	B-0	Prestonia	08h

Platform ID: 2 (Slot 2):

P6_0650.002	Pentium II Xeon	A-0		19h
P6_0652.002		B-0		29h
P6_0653.002		B-1		04h

P6_0671.002	Pentium III Xeon	A-1	Tanner	14h
P6_0672.002		B-0		21h
P6_0673.002		C-0		22h

P6_OF0A.002	Pentium 4	C-1	Willamette, 478-Pin Package	09h
P6_OF12.002	Pentium 4	D-0	Willamette, 478-Pin Package	03h
P6_OF21.002	Pentium 4	A-1	Northwood, 478-Pin Package	01h
P6_OF23.002	Pentium 4	A-2	Northwood, 478-Pin Package	08h
P6_OF24.002	Pentium 4	B-0	Northwood, 478-Pin Package	04h

Platform ID: 3 (Mobile Module):

P6_0650.003	Pentium II	dA-0	Deschutes	33h
P6_0652.003		dB-0		15h
P6_066A.003	Celeron	A-0	Dixon	04h
P6_0680.003	Pentium III	cA-0	Coppermine	16h
P6_0681.003		cA-2		0Fh
P6_0683.003		cB-0		08h
P6_0686.003		cC-0		09h

Platform ID: 4 (FC-PGA):

P6_0665.004	Celeron	mB-0	Mendocino	03h
P6_0681.004	Pentium III	cA-2	Coppermine	11h
P6_0683.004	Pentium III / Celeron	cB-0		14h
P6_0686.004	Pentium III	cC-0		08h
P6_068A.004	Pentium III	cD-0		01h

P6_0692.004	Celeron	A-2	Timna	03h
P6_0696.004		B-0		01h

P6_06B0.004	Pentium III	tA-0	Tualatin	04h
P6_06B1.004	Celeron	tA-1		1Ch

All patches above are also included in file **P6_INTEL.000**. It is used for boards with INT15 microcode patch update methode, i.e. D970, D1031, D1081, D1111, D1114